

# VLBI DATA Acquisition Terminal modernization at the Deep Space Network



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# Summary

- VLBI at Deep Space Network
- DSN VLBI Processor –DVP- Overview
- Required DVP functionality: customers support
- Automated VLBI Operations with the DVP



# DVP effort working group

- **JPL group:** Robert Navarro, Steven Rogstad, Eric Clark, Chuck Naudet, Chris Jacobs, Chuck Goodhart, Les White, Joseph Trinh, Melissa Soriano, Doug Wang, Elliot Sigman
- **DSN operations** (ITT Exelis Systems Division): John Luvalle, George Martinez
- **Canberra DSCC station:** Shinji Horiuchi, Phil Pope
- **Goldstone DSCC station:** Larry Snedeker
- **Madrid DSCC station:** Cristina Garcia-Miro, Ioana Sotuela, Juan Lobo

# VLBI at the DSN

- **Internal customers:**

- JPL Reference Frame Calibration project:
  - Earth Orientation Parameters determination: JPL TEMPO experiments, once every two weeks
  - Maintenance of inertial celestial reference frame for JPL navigation: S/X and X/Ka bands (The Celestial Reference Frame at X/Ka-band talk)
- JPL Delta Differential One-way Ranging (DDOR): support JPL navigation group
- Proof-of-concept of VLBI applications for navigation: phase referencing, same beam interferometry, etc.

- **External customers:**

- EVN + global observations: DSN is an associate member of the EVN
- IVS Geodesy and Astrometry observations
- Australian VLBI observations: Australian Long Baseline Array –LBA-

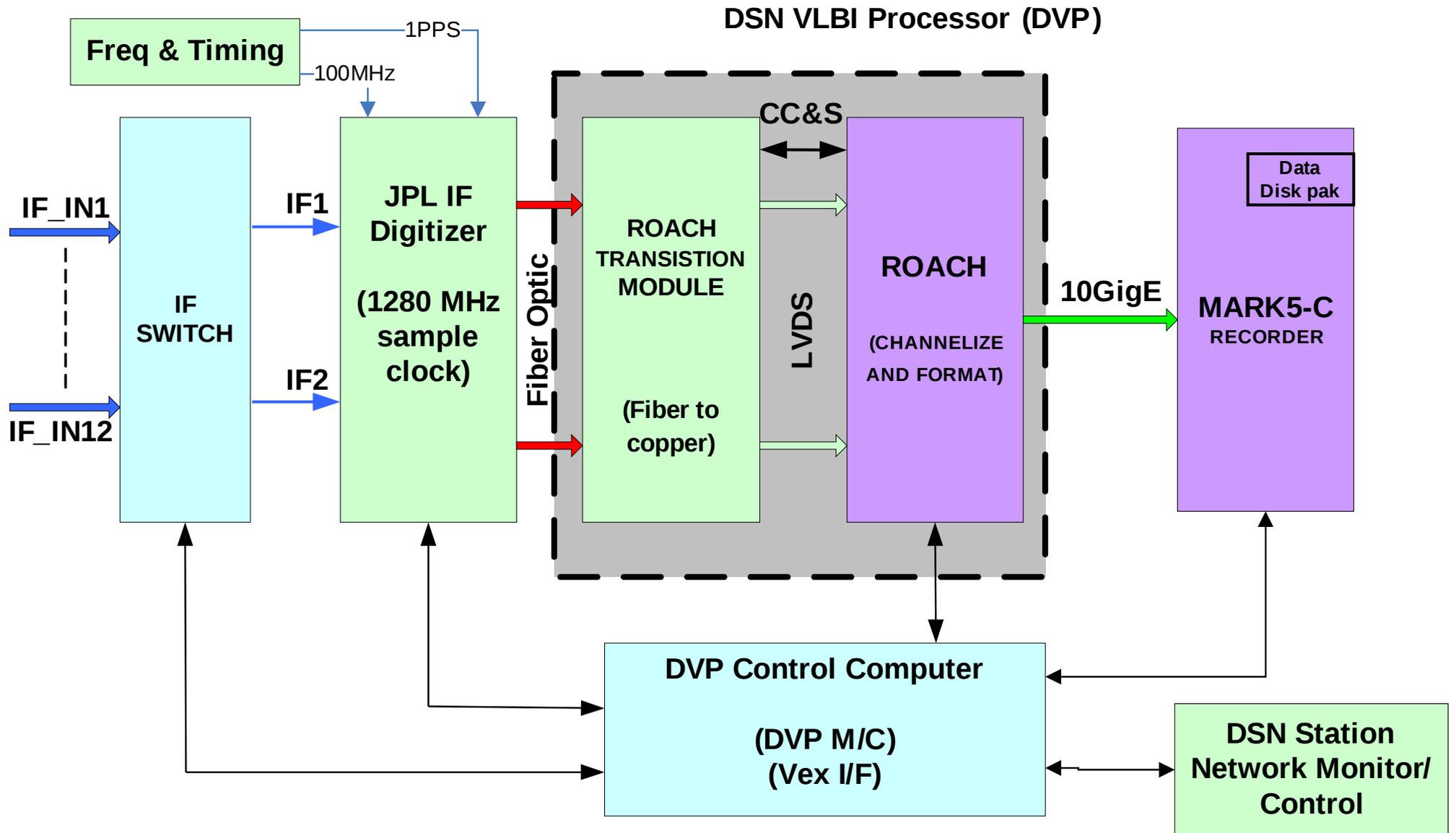
- **Other non-VLBI customers:**

- GBRA Host Country groups: single dish spectroscopy
- GBRA Guest Observe Programs: pulsars, DSN transient observatory, etc.

# Aim of the DVP

- Replace aging VLBI Data Acquisition Terminal hardware (MarkIV DAT) with modern Digital Backend system based on JPL Wideband VLBI Science Receiver (WVSR).
- Replace PCFS computer by *Dell PowerEdge R210 server as Data Processor and Controller (DPC) computer (debian linux)*. Replace Field System application by *driver, command, modeling and monitor and control WVSR based s/w*.
- Upgrade to Mark5C recorder for data recording.
- Make incremental improvements to JPL VLBI Software Correlator to support Mark5C hardware and data formats.
- Maintain compatibility with other VLBI centers for DSN support of international VLBI and Host Country activities.

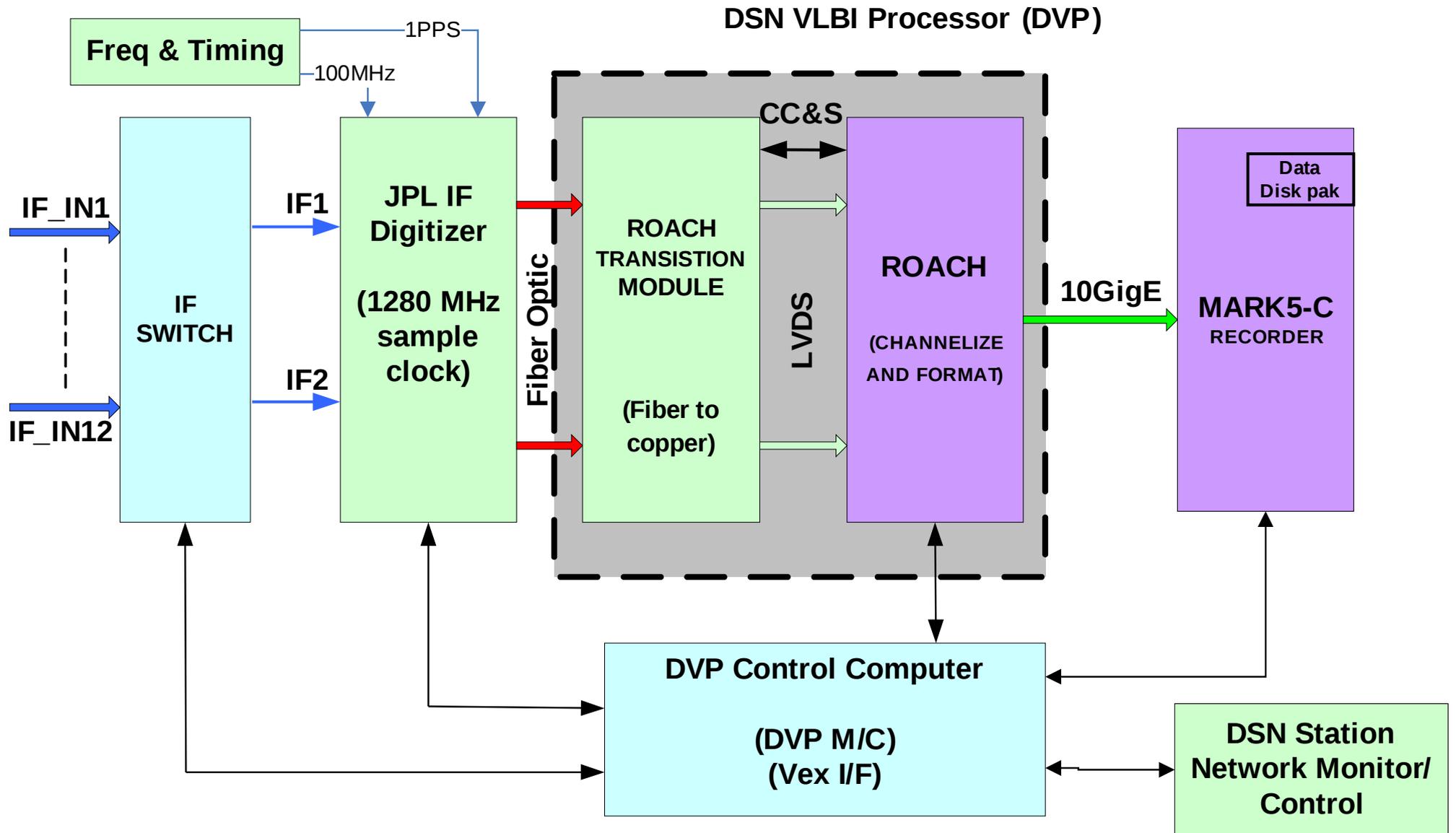
# DVP overview



# DVP overview

- IF switch up to 12 IF inputs from DSN antennas (at least 3 antennas at each complex support VLBI)
- Two IF inputs, each covering up to 500 MHz of bandwidth.
- Accepts DSN IF input band of 100-600 MHz. Good for L band (1.4-1.9 GHz), S band (2.3 GHz), X band (8.4 GHz), K band (18-26 GHz), Ka band (31.2 GHz) and Q band (38-50GHz).
- Uses JPL IF sampler module and CASPER ROACH board for Digital Processing and Channelization.
- Interfaces to JPL Deep Space Network monitor & control infrastructure.
- Records up to 32 upper/lower or 16 complex channels (in-phase and quadrature-phase). Channel max BW is 16 MHz (or 32 MHz for complex channels). Supported bits per channel = 8, 4, 2 or 1 bits.
- Phase calibration signal real time detection.
- Mark5C recorder used for data recording. Data stored on Mark5 modules in VDIF format.
- VEX 2.0 files used for input.
- Compatible with other digital developments (DBBC, RDBE, etc.)

# DVP overview



# DVP: JPL IF digitizer module

- Already in operational use at the DSN.
- A/D samples 8 bits at 1280 MHz.
- Digitally controlled built-in attenuator.
- Optically isolated from digital processing back ends:
  - Spurious signals attenuated 97 dB below A/D saturation level: S/C tracking
  - Enables use for spectral line work.
- Generates 1280 MHz sampling clock from 100 MHz reference.
- Uses interface module to connect to ROACH Board.

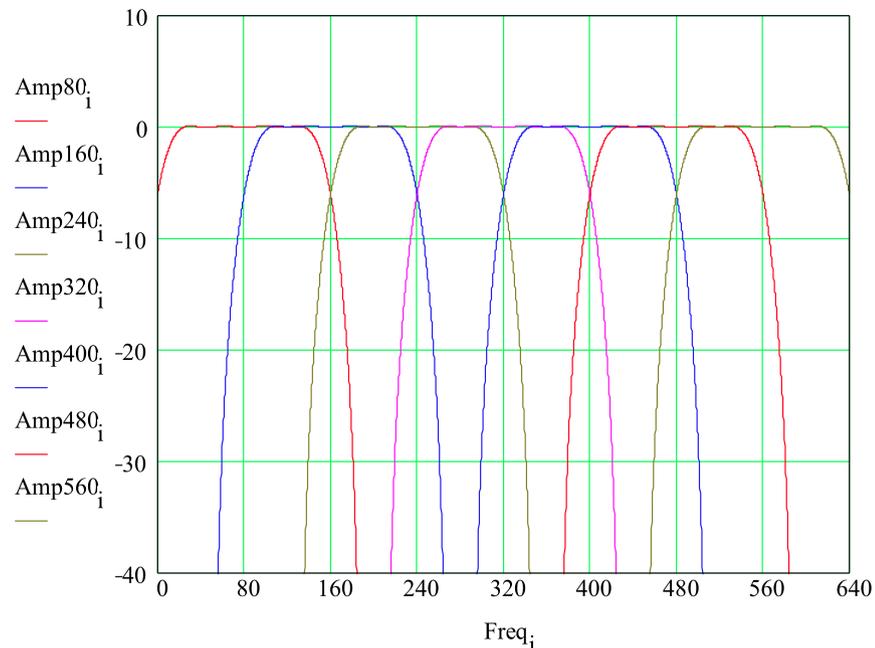


# DVP channelization and sub-band filtering

- JPL VLBI DAT Digital Backend Channelization broken up into two stages:
  - **First stage:**
    - polyphase filterbank breaks input signal up into 7 fixed bands of data, each 160 MHz (complex).
    - Channels centered at 80, 160, 240, 320, 400, 480 and 560 MHz.
  - **Second stage:**
    - selects one of seven first stage wideband inputs.
    - applies digital mixer for precise channel location selection.
    - Cascade of downconverting filters (CIC & FIR) provides variable output bandwidth per channel.
    - A total of 16 complex sub-channels can be formed (32 MHz to 1 KHz).
    - Changed to upper/lower representation using Hilbert transforms. Processing organization means upper/lower channels always occur in contiguous pairs.

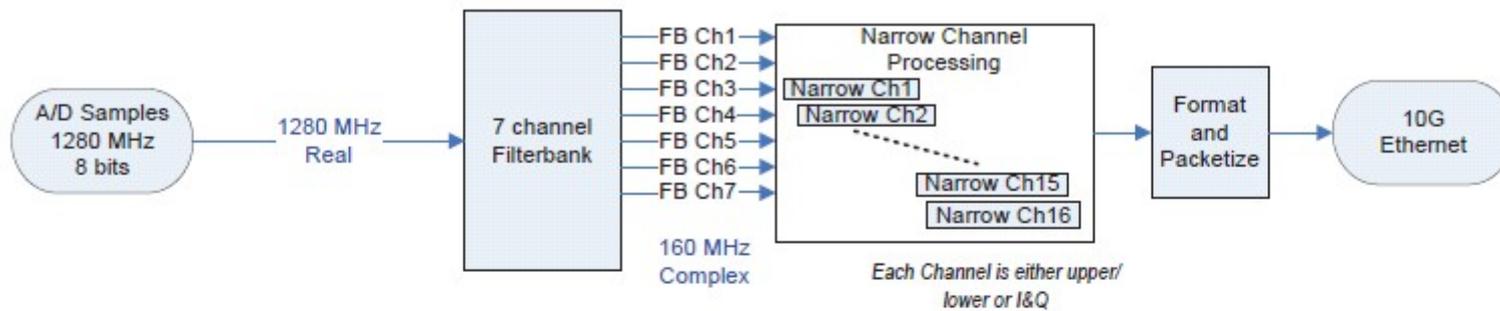
# DVP channelization and sub-band filtering

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  - **First stage:**
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  - **Second stage:**
    - selects one of seven first stage
    - applies digital mixer for precise
    - Cascade of downconverting filter bandwidth per channel.
    - A total of 16 complex sub-channels
    - Changed to upper/lower representation means upper/lower



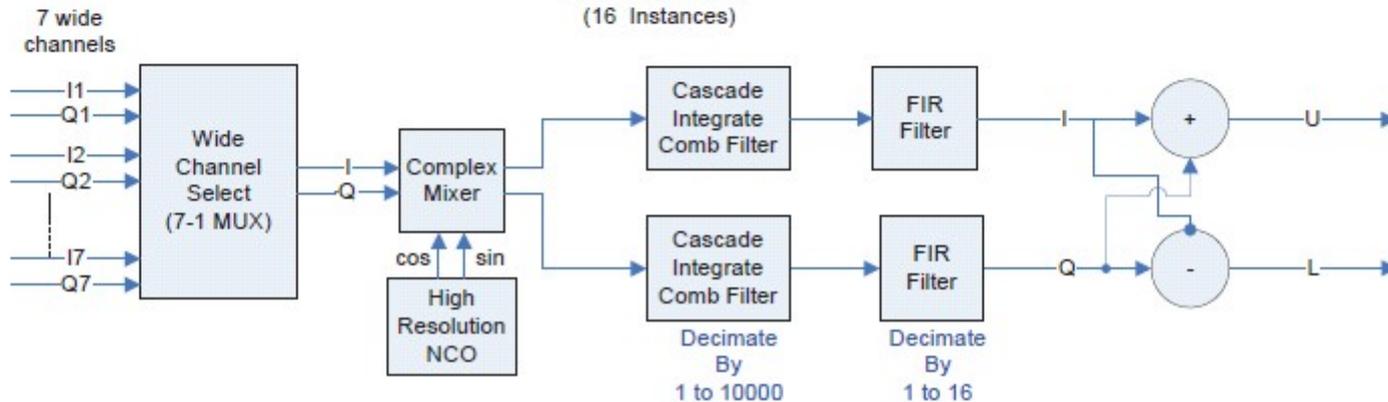
# Sub-band filtering stages

Wideband Processing:  
First Stage Downconversion  
Using polyphase Filterbank



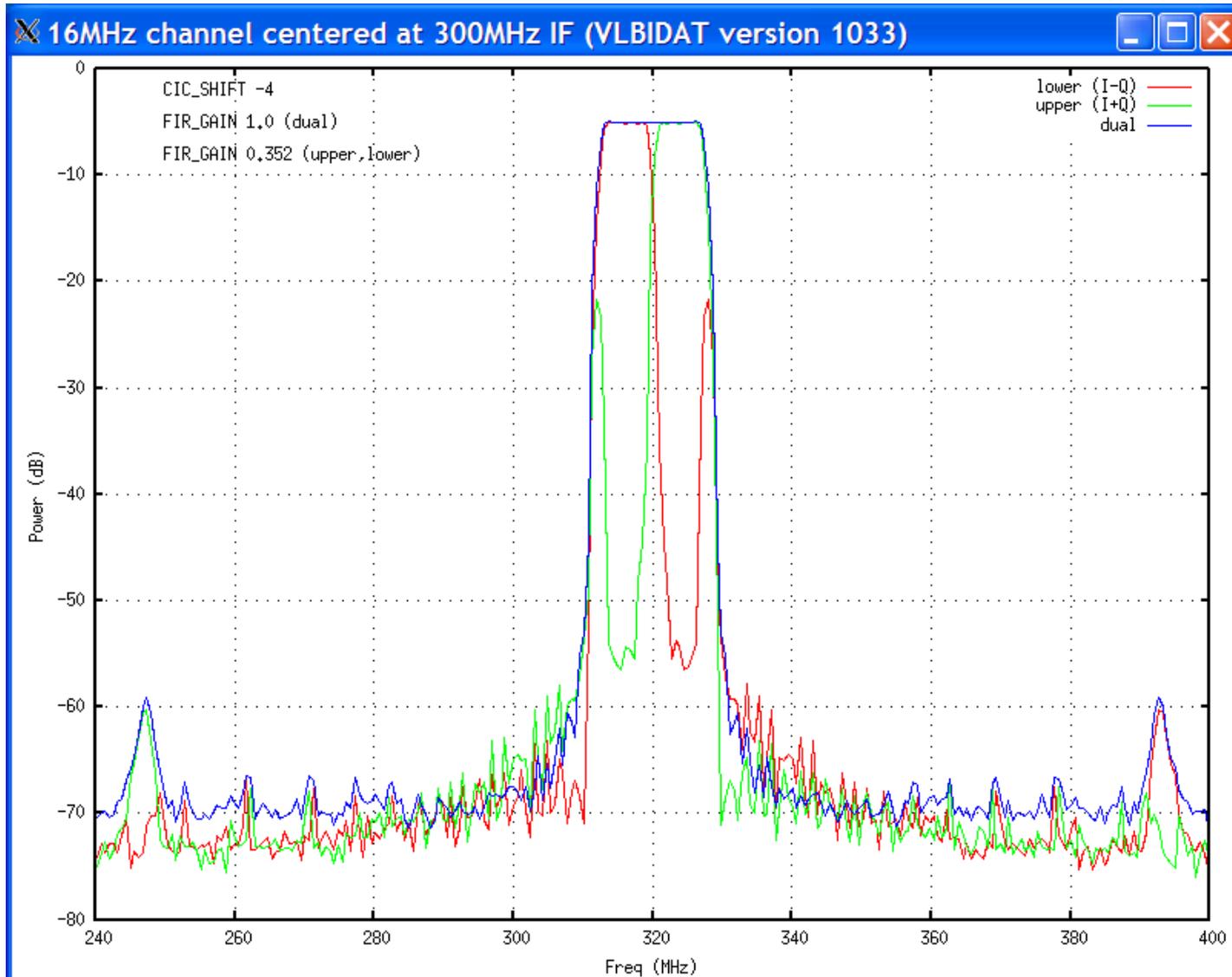
## Narrow Channel Processing

32 MHz to 1 KHz  
(16 Instances)



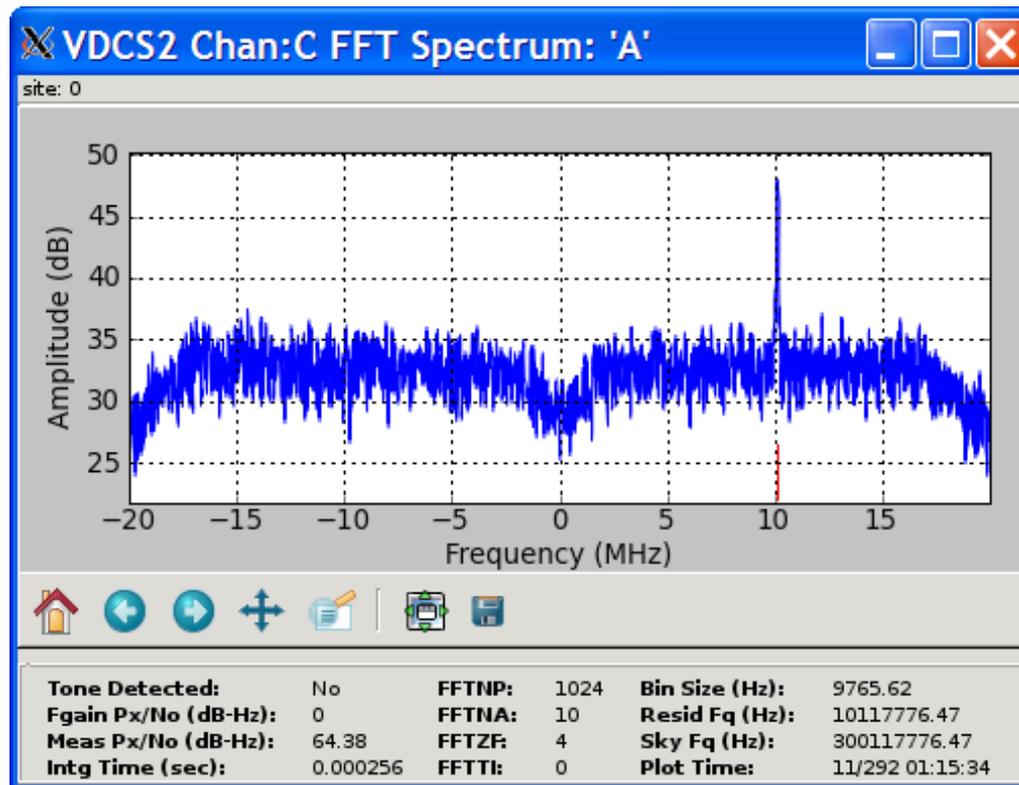
# Sub-band filter performance

- Aim is to get 0.1 db ripple in passband and at least 40 db attenuation in stopband.



# SubChannel output

- Actual data from tone buried in noise shown below. Sub-channel is 16 MHz upper / 16 MHz lower.
- Cutoffs of Upper and Lower bands apparent at edges.



# DVP overview



- First prototypes to be installed during Spring 2012 at Ro & Go
- DVP piggyback recording for some time
- Recording tests @ 2 Gbps without dropout during 8 hours

# DVP overview

1	Blank	44	77.00			44
	Blank	43	75.25			43
		42	73.50			42
		41	71.75			41
		40	70.00			40
		39	68.25			39
6		38	66.50			38
	Blank	37	64.75			37
2		36	63.00			36
	Blank	35	61.25			35
		34	59.50			34
		33	57.75			33
4		32	56.00			32
	Blank	31	54.25	1	1G- switch	31
2		30	52.50			30
	MARK 5C	29	50.75			29
		28	49.00			28
		27	47.25			27
		26	45.50			26
5		25	43.75			25
	ROACH	24	42.00			24
2		23	40.25			23
1	Blank	22	38.50			22
1	KVM	21	36.75			21
1	DPC	20	35.00			20
	Blank	19	33.25			19
2		18	31.50			18
1	FAN	17	29.75			17
		16	28.00			16
		15	26.25			15
	IFD	14	24.50			14
4		13	22.75			13
1	Blank	12	21.00			12
	IFD	11	19.25			11
	POWER	10	17.50			10
	SUPPLY	9	15.75			9
		8	14.00			8
4		7	12.25	2	VIP	7
1	Blank	6	10.50	2	FTS & SPC-LAN	6
		5	8.75	2	VIP	5
3	IF	4	7.00	2	IF inputs	4
	SWITCH	3	5.25	2	VIP	3
1	Blank	2	3.50	2	IF inputs	2
	POWER	1	1.75			1
2	panel					

# DVP GUI

**DVP1 Configuration** [Minimize] [Maximize] [Close]

Display Graphics Directives

site: 11 12/048 00:28:20

### Main Status

Status: **Operational**

DSS ID: 0

Ant Status: None

Source ID: None

Source RA: 0.0 deg

Source DEC: 0.0 deg

### IF Input A

Input Power: 0.0 dBm

ADC Amp: **-18.84**

ADC RMS: 10.26

ADC Peak: 16

Attenuator Setting: 3 dB

IF Source: Not Set

RF to IF LO: 8100 MHz

RF Freq: 8420000000.00 Hz

### IF Input B

Input Power: 0.0 dBm

ADC Amp: **-41.78**

ADC RMS: 0.73

ADC Peak: 2

Attenuator Setting: 15 dB

IF Source: Not Set

RF to IF LO: 8100 MHz

RF Freq: 8420000000.00 Hz

### Mark5 Status

State: IDLE

Bank A State: Empty

Bank B State: Empty

Bank A Free: N/A

Bank B Free: N/A

Cfg Bitrate: 0000.0 Mb/s

### Command

Select Script...

IF Switch...

Attenuator...

RF FREQ...

Mark5 Record...

### Channel Status

Chan ID	Status	Input ID	PCal Tone	PCal Drift (deg/min)	PCal Resid (deg)	PCal Avg Mag	Power (dBm)	Offset (Hz)	BW	Bits	FGAIN Mult
C	None	0	0	0.0	0.0	0.0	0.0	0	0	0	0.0
0	None	0	0	0.0	0.0	0.0	0.0	0	0	0	0.0

### Selected Channel Commands

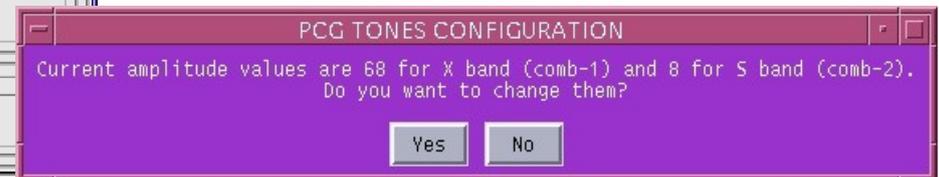
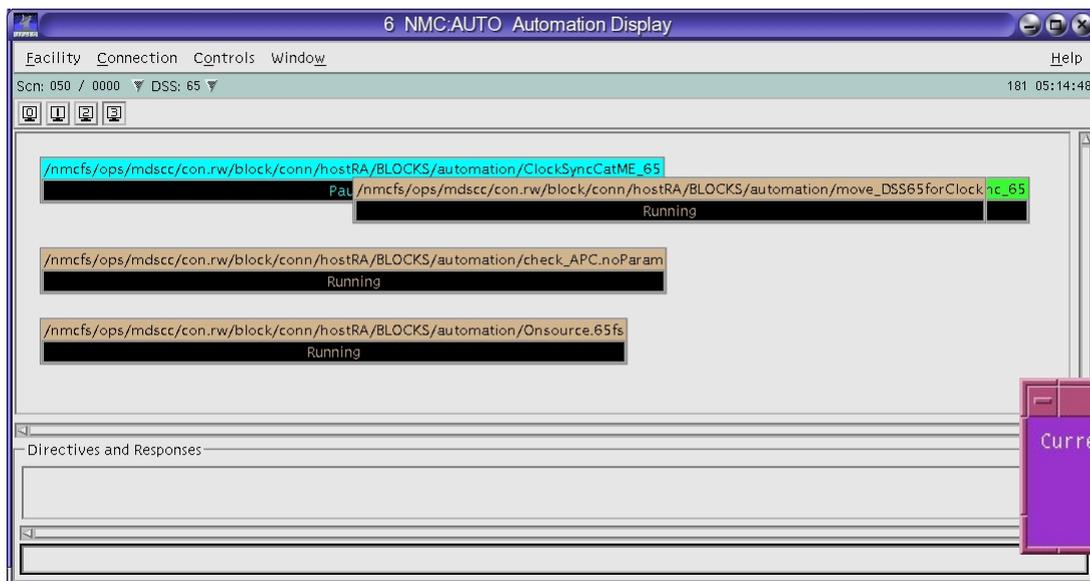
Configure Channel... Frequency Offset... Filter Gain... Data Histogram Plot... FFT Spectrum Plot...

# Required DVP functionality

- VEX 2.0 files used for input, should contain appropriate \$blocks for DVP configuration and precess coordinates for observing date.
- Antenna calibration performed using custom built DSN tools (ACME): gain curve, DPFU and noise diode versus frequency measurements substitute onoff / gnplt / rxgfiles.
- DVP log XLATOR to Field System type log:
- System temperature calibration: total power on each channel calculated at digital stage, IF total power measured using power meters. Output in ANTAB format.
- Phase calibration signal: real time tone extraction, in /pcald/ notation.
- Antenna status: provide /onsource/ and /flagr/ status.
- Weather: provide /wx/ notation.
- Mark5C monitor data.
- Equivalent gps-fmout: DVP digitizer does not provide 1pps output from internal clock.
- Automatic delivery of logs to users (IVS servers, vlbeer, etc).

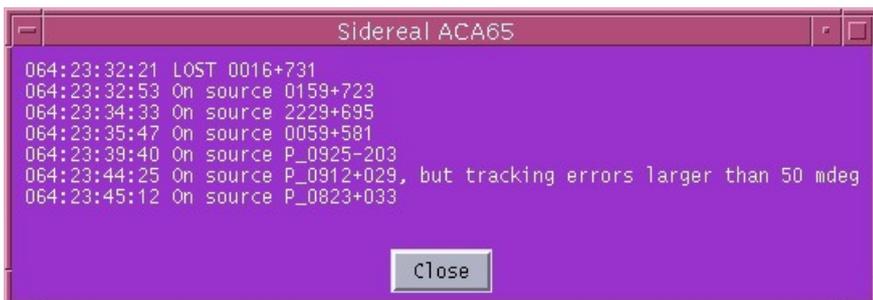
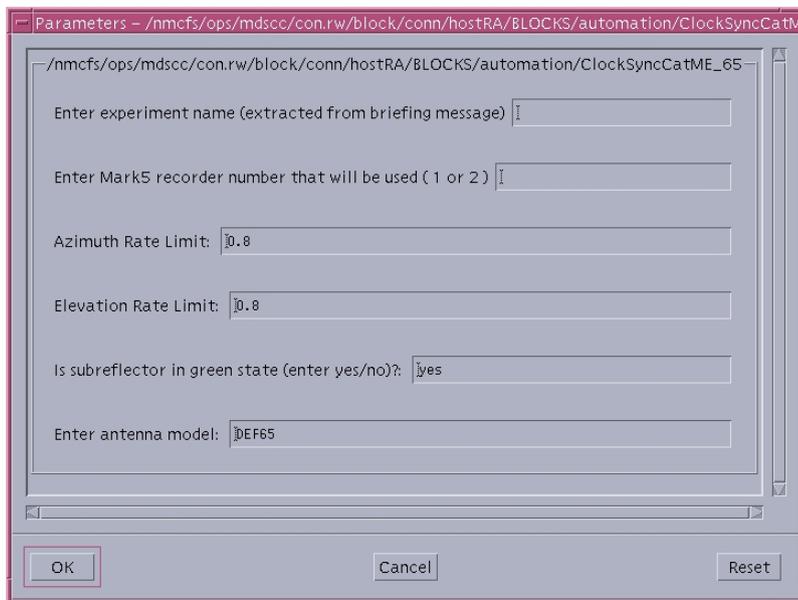
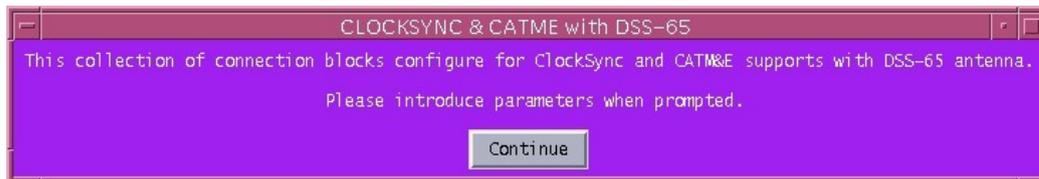
# Automated VLBI operations with the DVP

- DVP Interfaces to JPL Deep Space Network monitor & control infrastructure (NMC).
- NMC interfaces with all DSN subsystems (microwaves control, noise diode control, etc.): directives, responses and monitor data.
- NMC automation scheme: Connection Blocks and TDNs in Automation Language for Managing Operations (ALMO), superset of Tcl/Tk.
- Ability for simultaneous subsystems configuration, using closed loop control (directive/response), reducing manual input and critical operator errors.
- Pre-pass, during pass and post-pass tasks performed using connection blocks.



# Automated VLBI operations with the DVP: example

- Example of a pre-pass VLBI experiment supported with DVP and NMC automation:



- **pre\_VLBI\_XS\_65**: checks station time delay, checks necessary equipment in the link, executes DVP script building software to produce a DVP command script and a DSN antenna pointing file from VEX file
- **starting\_DVP\_VLBI\_XS\_SGP\_65**: start DVP application
- **conf\_micro\_VLBI\_XS\_SGP\_65**: configures microwaves to sky
- **ZenCal65\_VLBI\_XS\_SGP**: configures noise diode and executes Zenith calibration
- **conf\_PCGM\_65**: configures and turn on phase calibration signal
- **schedule\_start\_65**: starts DVP schedule, sends READY email
- **move\_DSS65forRFC**: configures and moves DSS65 antenna to sources, checks antenna status and fires noise diode at appropriate times, checks Mark5 status, sends START email

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Many thanks!



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